
Objectives: This course will be an introduction to the fields of assembly language and computer architecture. The student in this course will learn the architecture of a specific microprocessor and how to implement programs in an assembly language for that machine. The processor used in this course will be the 8086. Along the way, such topics as machine language, other processors (like the 80286, 80386, LSI-11, VAX, MC68000, and SPARC,*) and interfacing with high-level languages will be discussed.

Grading: There will be four major exams during the course of the semester. The highest three will be totaled and counted as 45% of the grade. There will be a final exam which will count as 25% of the grade. There will be several lab assignments which will count as 30% of the grade. No makeup exams will be given. You can miss one exam, because the grade of 0 will be dropped. You cannot miss the final. Late lab assignments will be accepted up to one week from the due date with a 20% late penalty.

The following grade scale is used:

\[ 90 \leq A \leq 100 \\
80 \leq B < 90 \\
70 \leq C < 80 \\
60 \leq D < 70 \\
0 \leq F < 60 \]

Absences: You are required to attend class and are responsible for all material covered in every class. It is the student’s responsibility to obtain notes, assignments, etc., from fellow class members.

Academic Integrity: All students must fully develop their own solutions. You are not allowed to work together on any assignment. Do not copy anyone else’s assignment and do not allow your assignments to be copied. Cheating will result in a grade of zero.